



SUBJECT DATASHEET

Semester:	2010/11/1
Subject:	Logical Circuits
Code:	VEMKFIB134E
Responsible department:	Institute of Physics and Mechatronics
Responsible department code:	MKFI
Responsible lecturer:	dr. Péter Gurin

Educational objectives:

Logical circuits

Detailed content of the subject:

1. Soldering practice. 2. Preparing practice. 3. Standard TTL and CMOS gates. 4. Combinational logic. 5. Scope of combinational logic. 6. Open collector and totem pole outputs. 7. Sequential logic. 8. Clock generators. 9. Counters, shift registers. 10. Counter-display project. 11. Inputs of logical circuits. 12. Usage of the outputs of a logical circuit. 13. Optoelectronic project 1. 14. Optoelectronic project 2. 15. Preparing of printed circuit.

Requirements:

Fulfill the main tasks in the measurements and provide a written results and analysis.

Required and suggested references:

Tietze-Schenk: Analóg és digitális áramkörök