UNIVERSITY OF PANNONIA

COURSE DATASHEET

Semester:	2015/16/1
Course:	Logical Circuits
Code:	VEMKFIB134E
Responsible department:	Institute of Physics and Mechatronics
Department code:	MKFI
Responsible instructor:	dr. Péter Gurin

Course objectives:

Logical circuits

Course content:

1. Soldering practice. 2. Prepairing practice. 3. Standard TTL and CMOS gates. 4. Combinational logic. 5. Scope of cmbinational logic. 6. Open collector and totem pole outputs. 7. Squential logic. 8. Clock generators. 9. Counters, shift registers. 10. Counter-display project. 11. Inputs of logical circuits. 12. Usage of the outputs of a logical circuit. 13. Optoelectronic project 1. 14. Optoelectronic project 2. 15. Prepairing of printed circuit.

Requirements, evaluation and grading:

Fulfill the main tasks in the measurements and provide a written results and analysis.

Required and recommended readings:

Tietze–Schenk: Analóg és digitális áramkörök